

## AMENDMENTS TO THE CLAIMS

The following listing of claims replaces all prior versions, and listings, of claims in the application.

Claims 1-10 (Canceled)

11. (Currently amended) A field effect transistor (FET) configured to sense a property of a medium, the FET comprising:

a source contact;

a drain contact;

a gate contact, wherein the gate contact defines a gate area disposed below and adjacent the gate contact;

a dielectric layer disposed below the gate contact and extending beyond the gate contact to the source contact and the drain contact; and

a sensing layer for sensing the property, wherein the sensing layer is disposed below the ~~gate contact~~ dielectric layer and wherein the sensing layer is exposed to the medium in the gate area; and

~~a dielectric layer disposed between the gate contact and the sensing layer.~~

12. (Currently amended) The FET of claim 11, wherein the gate contact and the dielectric layer each include ~~includes~~ at least one perforation.

13. (Canceled)

14. (Previously presented) The FET of claim 11, wherein the dielectric layer includes at least one perforation in the gate area adjacent the gate contact.

Claims 15-20 (Canceled)

21. (Previously presented) The device of claim 11, wherein the sensing layer comprises gallium nitride (GaN).

22. (Previously presented) The device of claim 11, wherein the source and drain contacts are formed on the sensing layer.

23. (Previously presented) A semiconductor device configured to sense a property of a medium, the device comprising:

an electrical contact; and

a gallium nitride (GaN) sensing layer disposed below the contact, wherein the sensing layer is exposed to the medium in an area below the contact by at least one perforation in at least one of the contact and a second layer disposed between the sensing layer and the contact.

24. (Previously presented) The device of claim 23, wherein the contact includes the at least one perforation for exposing the sensing layer to the medium.

25. (Previously presented) The device of claim 24, wherein the at least one perforation comprises an area in the range from approximately one square nanometer to approximately ten square centimeters.

26. (Previously presented) The device of claim 23, further comprising at least one additional contact.

27. (Previously presented) The device of claim 23, wherein the device is configured to operate as a field effect transistor and wherein the contact comprises a gate contact.

28. (Previously presented) The device of claim 25, wherein the field effect transistor comprises a compound semiconductor field effect transistor.

29. (Previously presented) The device of claim 23, wherein the property comprises a pH level.

30. (Previously presented) The device of claim 23, further comprising the second layer disposed between the contact and the sensing layer.

31. (Previously presented) The device of claim 30, wherein the second layer includes the at least one perforation for exposing the sensing layer to the medium.

32. (Previously presented) The device of claim 23, wherein the sensing layer comprises a layer in an aluminum gallium nitride (AlGaN)/gallium nitride (GaN) heterostructure.

33. (Currently amended) A semiconductor device configured to sense a property of a medium, the device comprising:

an electrical contact that includes at least one perforation to expose a sensing layer to the medium in an area below the electrical contact;

a dielectric layer disposed below the contact; and

a nitride active structure disposed below the dielectric layer, wherein the sensing layer comprises at least one of: the dielectric layer and a semiconductor layer in the active structure.

34. (Previously presented) The semiconductor device of claim 33, wherein the dielectric layer includes at least one perforation.

35. (Previously presented) The semiconductor device of claim 33, wherein the nitride active structure comprises an aluminum gallium nitride (AlGaN)/gallium nitride (GaN) heterostructure.

36. (Previously presented) The semiconductor device of claim 33, wherein the device is configured to operate as a field effect transistor.

37. (Previously presented) The semiconductor device of claim 36, wherein the contact comprises a gate contact.

38. (Previously presented) The semiconductor device of claim 33, wherein the dielectric layer comprises at least one of: Silicon Dioxide (SiO<sub>2</sub>) or Silicon Nitride (SiN).